IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Dominik EISERT et al.

Serial No.:   10/572,655

Filed: March 6, 2007

For: Radiation-Emitting   Thin-Film   Semiconductor
        Chip

Examiner: LAM, Cathy N.
Group Art: 2811

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

S I R:

In compliance with the duty of disclosure under 37 C.F.R. §1.56 and in accordance with the practice under 37 C.F.R. §§1.97 and 1.98, the Examiner's attention is directed to the document listed on the enclosed Form PTO/SB/08a. The document is cited in an Office Action issued in U.S. Patent Application No.10/523,551 on November 23, 2009.

In accordance with 37 C.F.R §§1.97(g) and (h), the filing of this Information Disclosure Statement should not be construed as a representation that a search has been made or that information cited is, or is considered to be, material to patentability as defined in §1.56(b), or that any cited document listed or attached is (or constitutes) prior art. Unless otherwise indicated, the date of publication indicated for an item is taken from the face of the item and Applicants reserve the right to prove that the date of publication is in fact different.

This information is being submitted after the mailing of the first Office Action on the merits, but before the mailing of a final Action or the Notice of Allowance.

It is believed that no fees or charges are required at this time in connection with the present application. However, if any fees or charges are required at this time, they may be charged to our Patent and Trademark Office Deposit Account No. 03-2412.

It is respectfully requested that the above information be considered by the Examiner and that a copy of the enclosed Form PTO/SB/08a be returned indicating that such information has been considered.

Respectfully submitted,
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